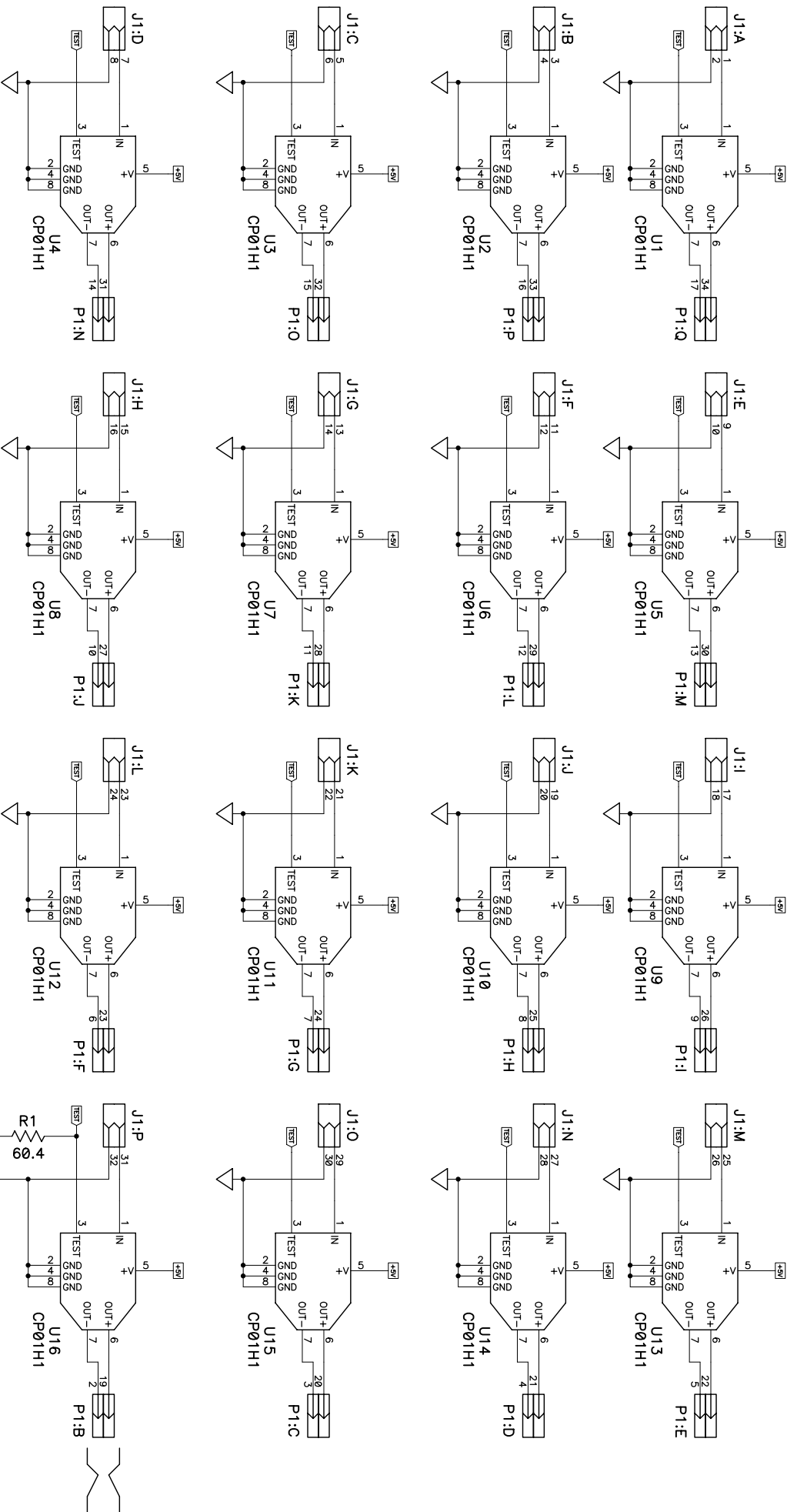
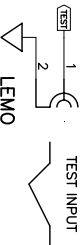
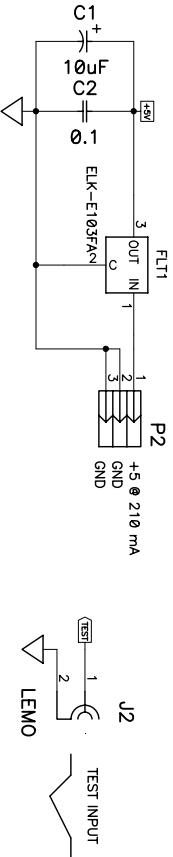




NOTE: P1 CONNECTIONS AND TEST INPUT POLARITY MUST BE REVERSED FOR CATHODE READOUT. PADS ON PCB ALLOW FOR P1 MOUNTING FOR ANODE OR CATHODE READOUT. THE TEST INPUT SIGNAL MUST BE POSITIVE WHEN BOARD USED FOR CATHODE READOUT.



Title			
HALL D Forward Drift Chamber Preamplifier			
Size	Number	Rev	
B	FDCPA		
Date	Filename	Drawn by	Sheet
31 March 2003	FDCPASCH	FJB	1