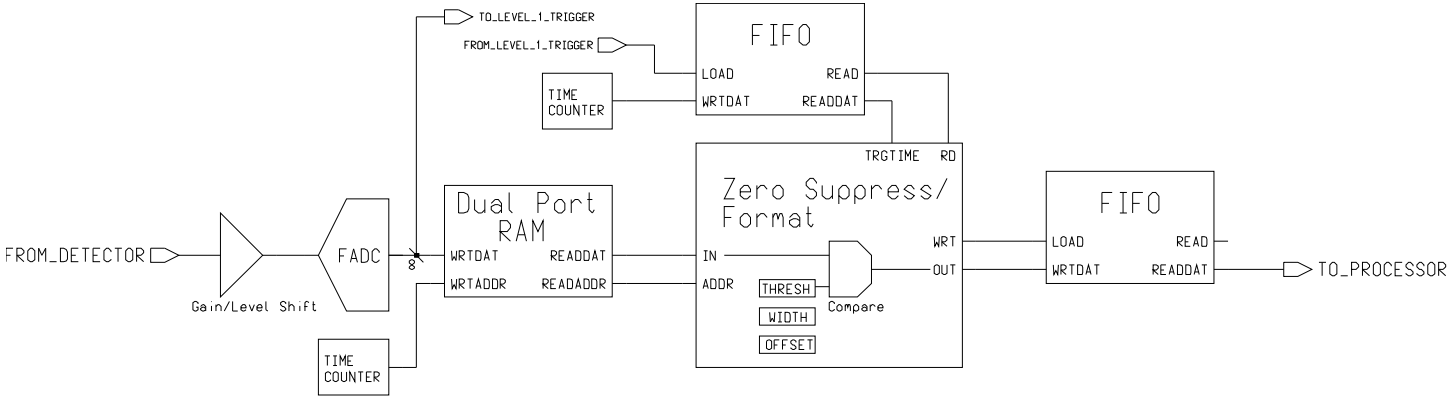
TO_LEVEL_1_TRIGGER
FROM_LEVEL_1_TRIGGER
FIFO
LOAD
READ
WRTDAT
READDAT
TIME COUNTER
TRGTIME
RD
Zero Suppress/ Format
WRT
OUT
IN
ADDR
THRESH
WIDTH
OFFSET
Compare
FIFO
LOAD
READ
WRTDAT
READDAT
TO_PROCESSOR
FROM_DETECTOR
FADC
Gain/Level Shift
Dual Port RAM
WRTDAT
READDAT
WRTADDR
READADDR
8
TIME COUNTER