

Hall D Electronics Forum

24 October 2007

Jefferson Lab

9:00 Welcome Remarks
9:05 Detector and Electronics Overview

Status Updates

9:30 ASIC
9:40 On-Detector Electronics (STBs, HVTBs)
9:40 Preamp Card
9:50 SiPMs & PMTs
10:00 **Break**
10:15 Cabling & Cooling
10:25 fADC125
10:50 fADC250
11:15 **Break**
11:25 Energy Sum Module

11:45 **Lunch**

1:00 Signal Distribution (Clock, etc.)
1:25 Level 1 Trigger Modules
2:00 **Break**

Work Session

2:10 VXS Backplane Assignments
Trigger
3:30 **Break**
3:40 DAQ Integration
4:10 Slow Controls (HV, LV, Temperature, etc.)
4:30 **Break**
4:40 What's Missing
5:00 Discussion and Tasks
5:30 Summary
6:00 Adjourn